


PATENT NUMBER

U.S. **UTILITY** Patent Application

<p>  </p> <p> O.I.P.E. </p> <p> SCANNED </p> <p> G.A. </p>	<p> PATENT DATE </p>
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APPLICATION NO. 09/922384	CONT/PRIOR D F	CLASS 029	SUBCLASS	ART UNIT 3729	EXAMINER <i>catb5th</i>
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APPLICANTS

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TITLE

Mounting structure of a semiconductor device, and a process for mounting a semiconductor device on a mounting substrate

PTO-2040
12/99

ISSUING CLASSIFICATION

[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig. 2	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED	
	_____ (Primary Examiner) (Date)		ISSUE FEE	
			Amount Due	Date Paid
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		ISSUE BATCH NUMBER	

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